



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Think Excellence. Live Excellence.

Shri Vaishnav Institute of Technology and Science
Department of Electrical and Electronics Engineering

Class Time Table - JULY-DECEMBER 2021

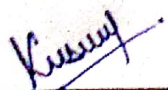

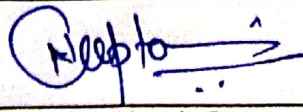
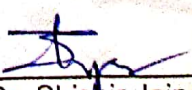
Doc No. : SVVV/AC/002 /1 Rev No. : 00
CLASS:2nd BRANCH :ELECTRONICS AND INSTRUMENTATION
SEM-III N Block: Electrical and Electronics Engineering
Class Co-ordinator:Ms. Alka Karketta
Rev. Date : Room No. :
EFFECTIVE FROM: ~~09~~ ²³ August 2021

TIME/DAY	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
L1 10:30 - 11:15	BTEC305	BTEC305	BTEC402(P)	BTEC302	BTEC305(P)	BTEC402(T)
L2 11:15 - 12:00	BTEC402	BTEC402	BTEC305(T)	BTEC305	BTEI302	BTEI302(T)
L3 12:00 - 12:45	BTEI302	BTEI302(P)	BTEC302	BTEC302(P)	BTEC302(T)	BTEC302
L4 12:45 - 01:30	ML301	ML301	ML301	ML301	BTMT302(P)	self study
L5 2:30-3:15						BTCS207(P)

Subject Code	Subject Name	Batch	Teacher Abbreviation	Teacher Name
ML301	ENVIRONMENTAL MANAGEMENT AND SUSTAINABILITY(400)		GS	Mr. Gyanesh Savita
BTEC402	DIGITAL ELECTRONICS(212)		GM	Mr. Gaurav Makwana
BTEI302	MEASUREMENT AND INSTRUMENTATION(212)		GS	Mr. Gyanesh Savita
BTEC305	ELECTRONIC CIRCUITS(312)		AK	Ms. Alka Karketta
BTEC302	NETWORK ANALYSIS AND SYNTHESIS(312)		VC	Mr. Vijay Chauhan
BTMT302	PCB DESIGNING(002)		GS	Mr. Gyanesh Savita
BTCS207	COMPUTER PROGRAMMING(002)		RP	Mrs. Reena Patidar

Faculty must remain present nearby lecture room 5 min before lecture starts.

T-Tuto,L-lab





 Kusum Tilkar , Jitendra Managre
Time Table Coordinator
EEE dept. SVITS
 Dr. Namit Gupta
Director,SVITS
SVVV
 Dr. Shishir Jain
Time Table Coordinator
SVVV